



- Endianness transparent on [32 bit] WORD accesses.
- Endianness 2 byte offset for [16 bit] SHORT accesses.
- Endianness 3 byte offset for [8 bit] BYTE accesses
- BIG endian architectures push *LSB downwards* on the external bus.
- LITTLE endian architectures push *MSB upwards* on the external bus.